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our goal was to use less hardware overhead than commonly used duplication or triplication methods to cope with the high sensitivity of electronic devices to failures or soft errors we also proposed a multiprocessor system on an dynamically reconfigurable architecture for the design of fault tolerant systems, **dynamic reconfigurable software architecture analysis and** - dynamic reconfigurable software architecture analysis evaluation 83 3 1 agent interface the interface of each agent is defined as the set of provided actions but also required events to each agent we attach event condition action rules mechanism in order to react with the architecture and the architectural, **dynamically reconfigurable embedded architecture an** - can be addressed by an emerging approach to system design namely the use of dynamically reconfigurable embedded architectures in the next section such a system is discussed in detail dynamically reconfigurable embedded architecture methodology a reconfigurable system changes its form in order to alter its function for this it must be , **an iterative method for algorithms implementation on a** - dnode are note based on the use of the dynamically reconfigurable fpga ardoise propose a dynamically reconfigurable architecture dedicated to embedded image and signal processing in 1 an image processing application image rotation that exploits the fpgas dynamic reconfiguration method is presented it, **dynamically reconfigurable systems architectures design** - p this book details the newest developments and trends in dynamic and run time reconfigurable systems it covers all aspects of the subject architectures design methods and applications p, **design methodology for dynamically reconfigurable systems** - this methodology is based on an adequation algorithm architecture where application is represented by a control data flow graph and architecture by an architecture graph we focus on how to take into account specificities of partially reconfigurable components during the adequation process and for the design generation, **dynamically reconfigurable architecture systems for time** - research in dynamically reconfigurable systems for time varying image constraints drastic led to the development of scalable architectures for video processing by switching hardware resources in real time drastic methods can provide very high performance while saving energy and communications bandwidth, **applied reconfigurable computing architectures tools** - architectures tools and applications 14th international symposium arc 2018 santorini greece may 2 4 2018 proceedings an intrusive dynamic reconfigurable cycle accurate debugging system for embedded processors pages 446 458 comparing c and systemc based hls methods for reconfigurable systems design konstantinos georgopoulos, **download reconfigurable computing architectures tools and** - systems and applications group esa koch esa tu darmstadt de architectures and design methods abstract recon gurable computing is becoming increasingly attractive for many applications member ieee and hartej singh member ieee abstract dynamically reconfigurable architectures are emerging as a viable design alternative to, **architecture and application of a dynamically** - dynamically reconfigurable architectures into soc solu tions for future generations mobile terminals is described in this work and demonstrated by the performance of analyzed computation intensive application parts within mobile communication systems the proposed reconfigurable soc architecture parts provide for the here selected appli, **evaluation and design methods for processor like** - dynamically reconfigurable systems dynamically reconfigurable systems pp 95 116 delosme j design space exploration for dynamically reconfigurable architectures in proc of the date pp 366 rosenstiel w 2010 evaluation and design methods for processor like reconfigurable architectures in platzner m teich j wehn n eds, **run time support for dynamically reconfigurable computing** - reconfigurable computing systems normally consist of an instruction set processor connected to a block of reconfigurable logic the reconfigurable logic for example an field programmable gate arrays fpga can usually be adapted during the run time of an application to perform different tasks, **recon gurable computing architectures and design methods** - able system architectures and design methods although we also provide background information on notable aspects of older technologies our focus is on the most recent architectures and design methods as well as the trends that will drive each of

these areas in the near future in other words we intend to complement other survey papers 13, **efficient reconfigurable regions management method for** - 71 efficient reconfigurable regions management method for adaptive and dynamic fpga based systems the csd permits to express the local color structure in an image manjunath et al 2001 vertan et al 2011 it aims to provide a more accurate description by identifying local color distribution, **9783659128370 optimization algorithms for reconfigurable** - abebooks com optimization algorithms for reconfigurable fpga based architectures fpga design flow reconfigurable architectures system on programmable chip 9783659128370 by bouraoui oui and a great selection of similar new used and collectible books available now at great prices, **488 ieee transactions on circuits and systems for video** - as for related work on single pixel processing architectures two methods are common lut based and custom hardware in 9 we presented a single pixel hardware 8 b input 8 b we report power as the sum of the dynamic and design static power see table i the design static power is a dynamically reconfigurable pixel processor system 499, **dynamically reconfigurable systems architectures design** - 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